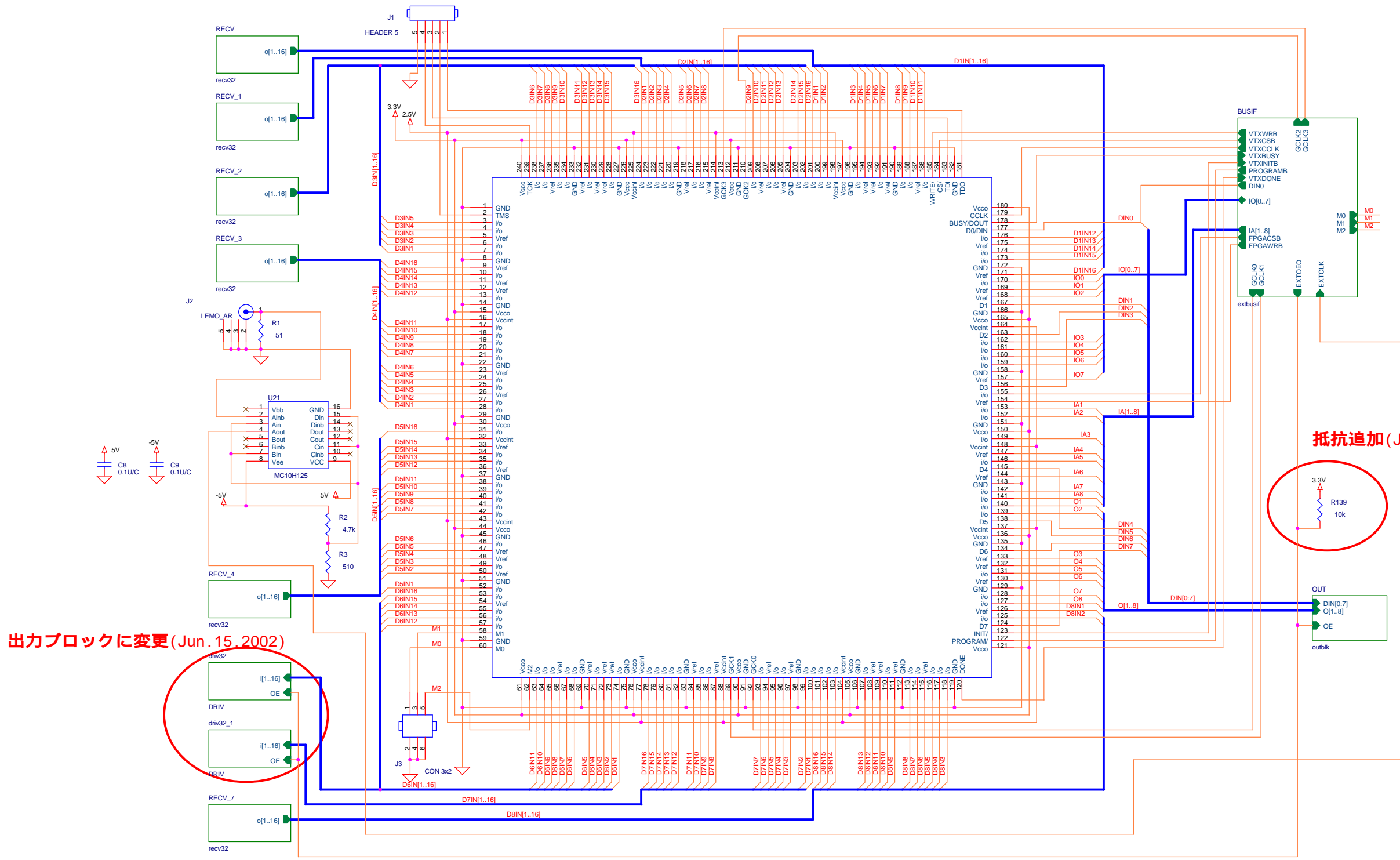
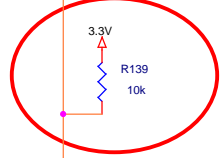


# TRG BOARD(for TOF) FINAL VERSION (6月15日 2002年)

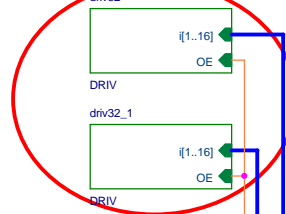
DIN[0:7]は出力ピン確保のためOUTBLKブロックへ繋いだ。(4月 26日 2002年)



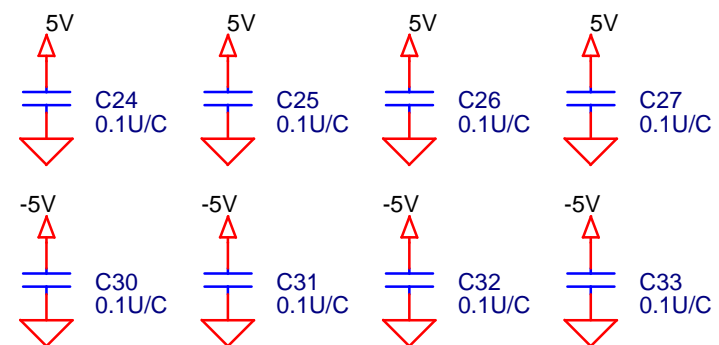
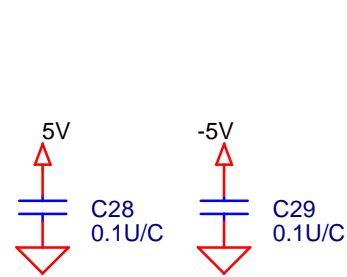
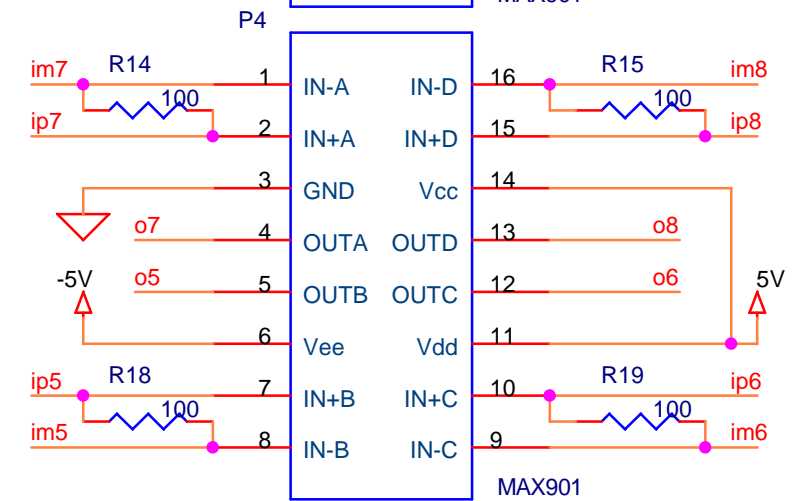
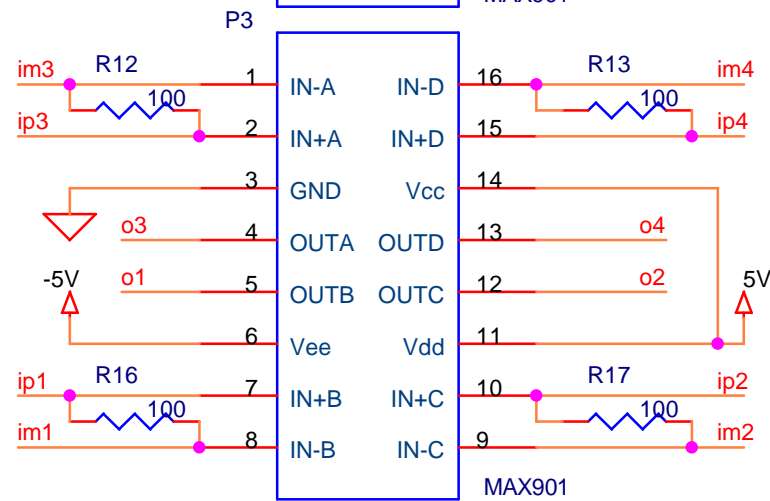
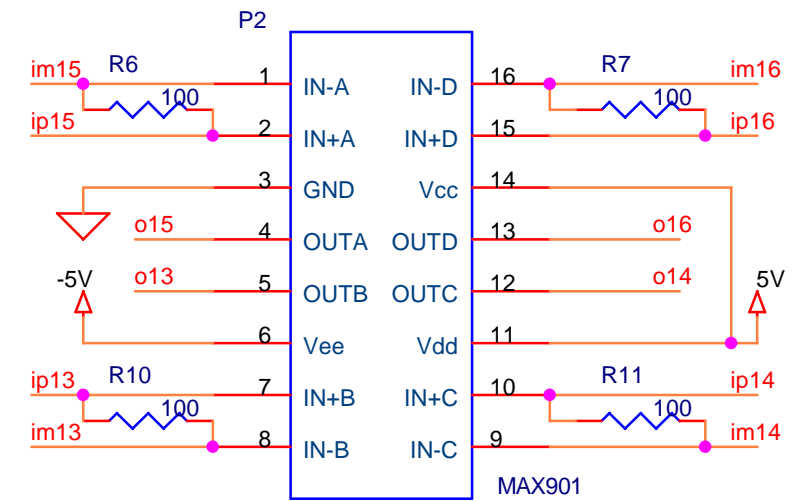
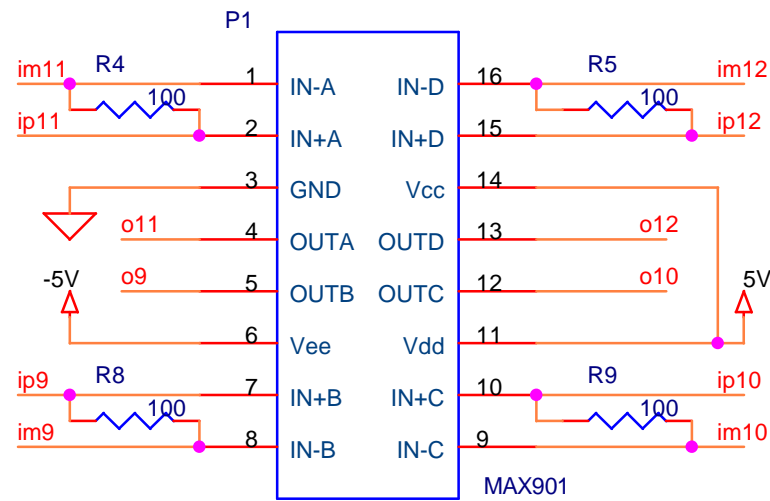
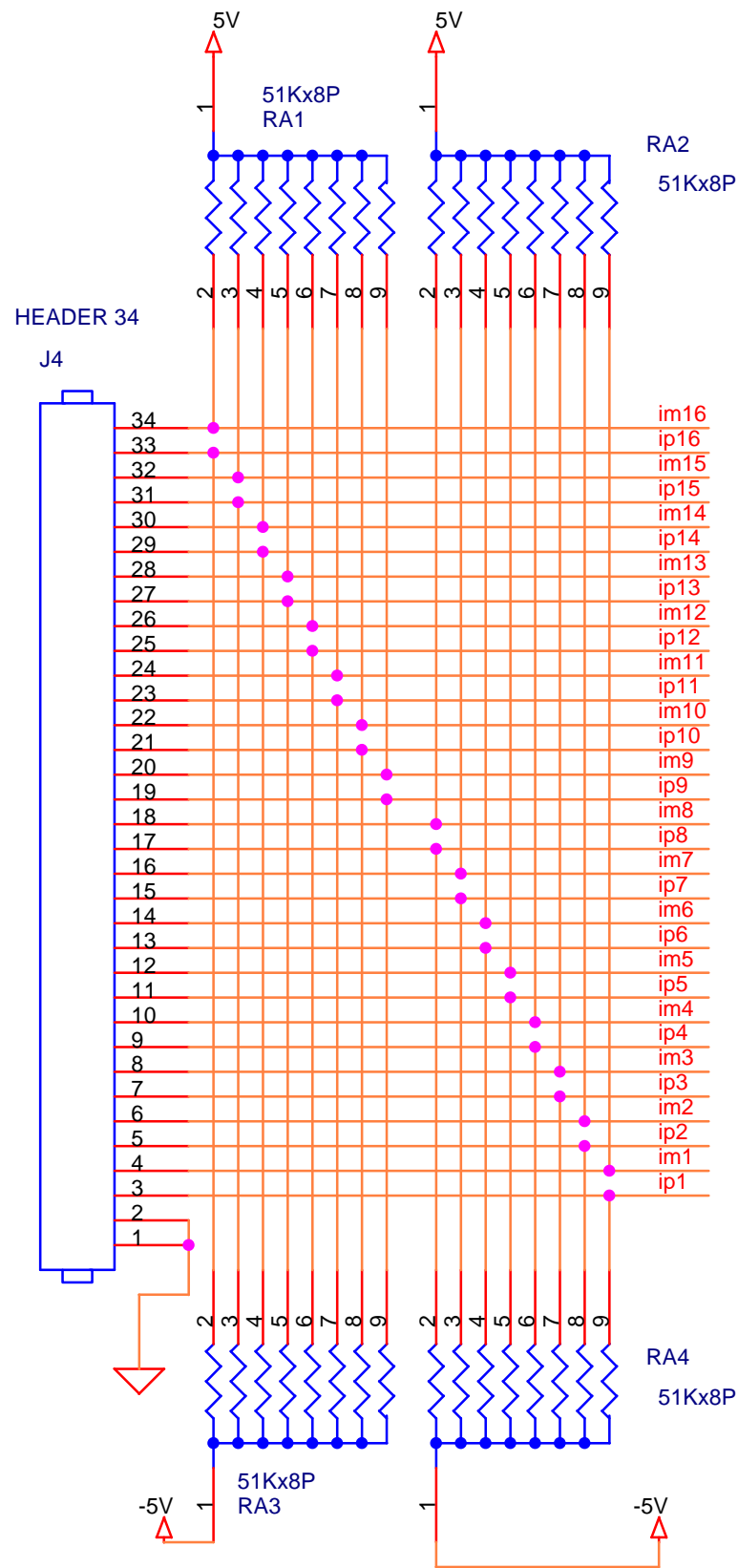
抵抗追加 (Jun. 15. 2002)



出力ブロックに変更 (Jun. 15. 2002)



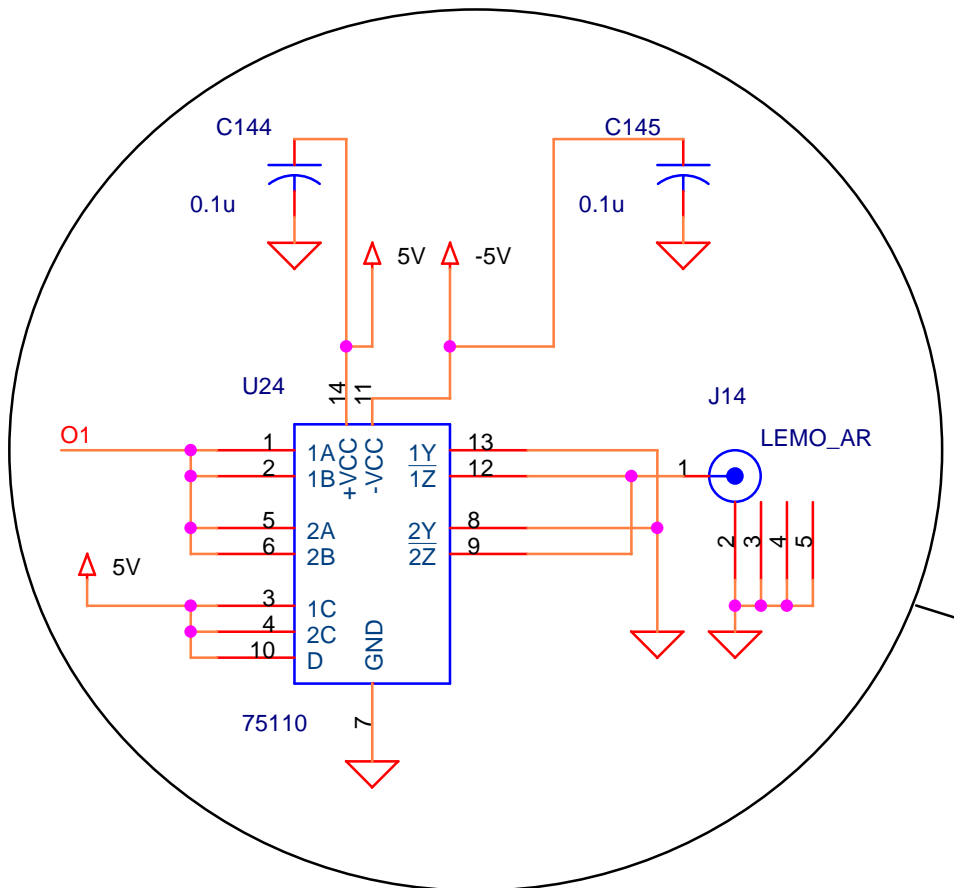
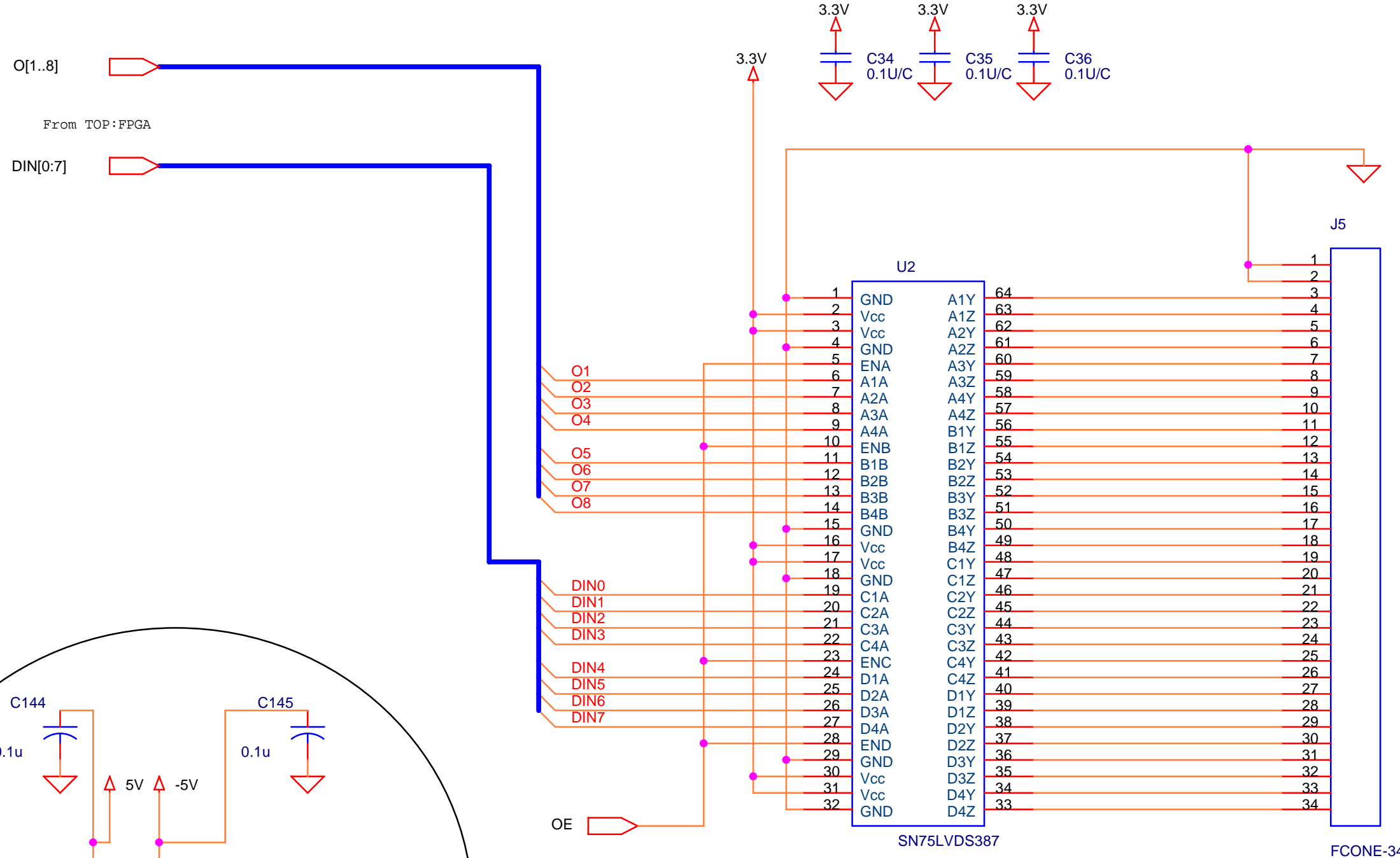
# ターミネータはなるべくICの近くへ実装



Check: connector pin assignement, receiver pin assignment  
5/29/2001

Title		
RECV16:receiver block		
Size	Document Number	Rev
A4	MT-02222001-GTRG	0
Date:	Sunday, January 27, 2002	Sheet 2 of 12

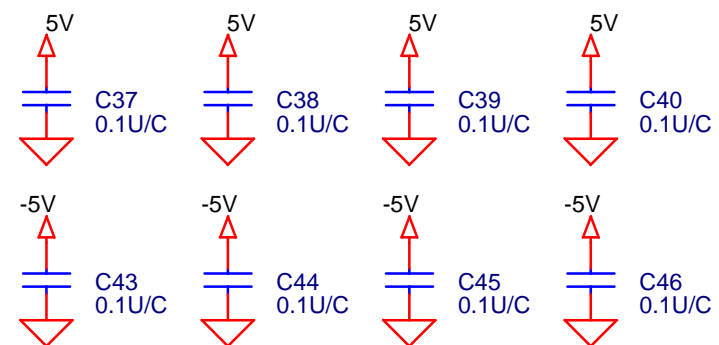
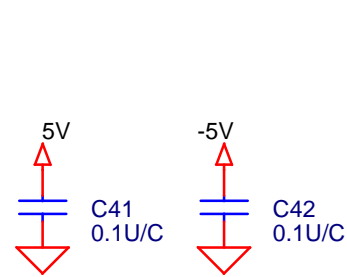
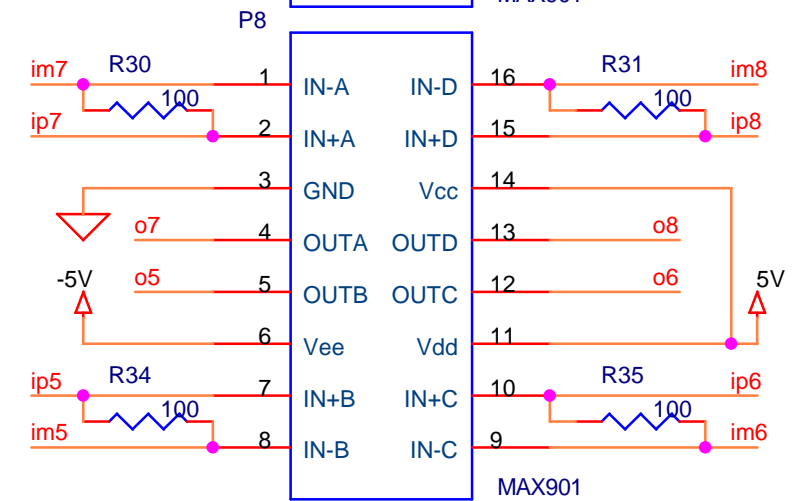
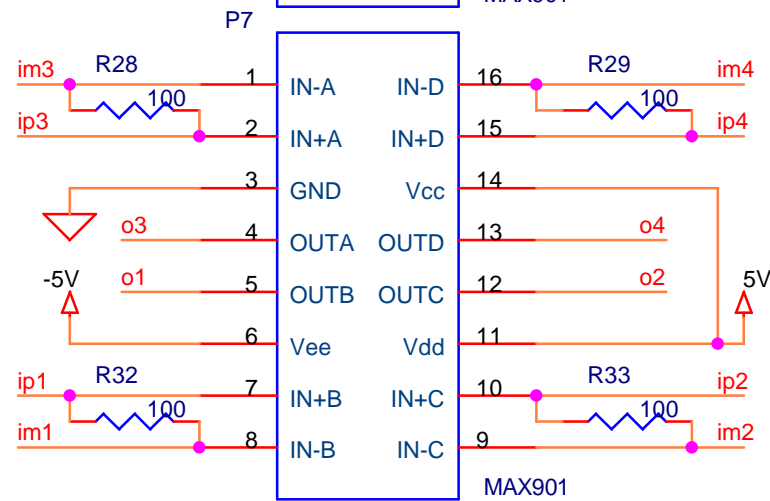
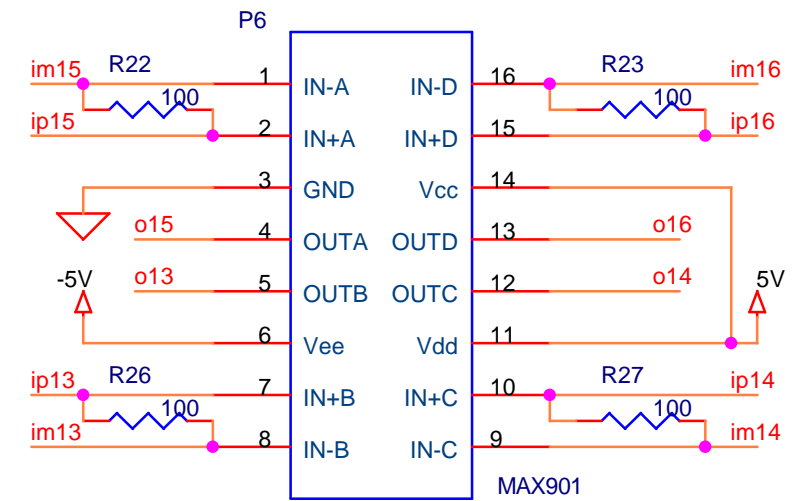
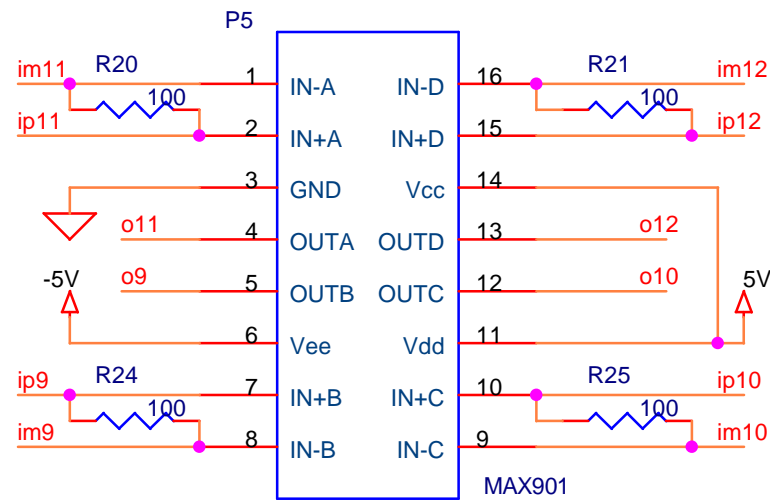
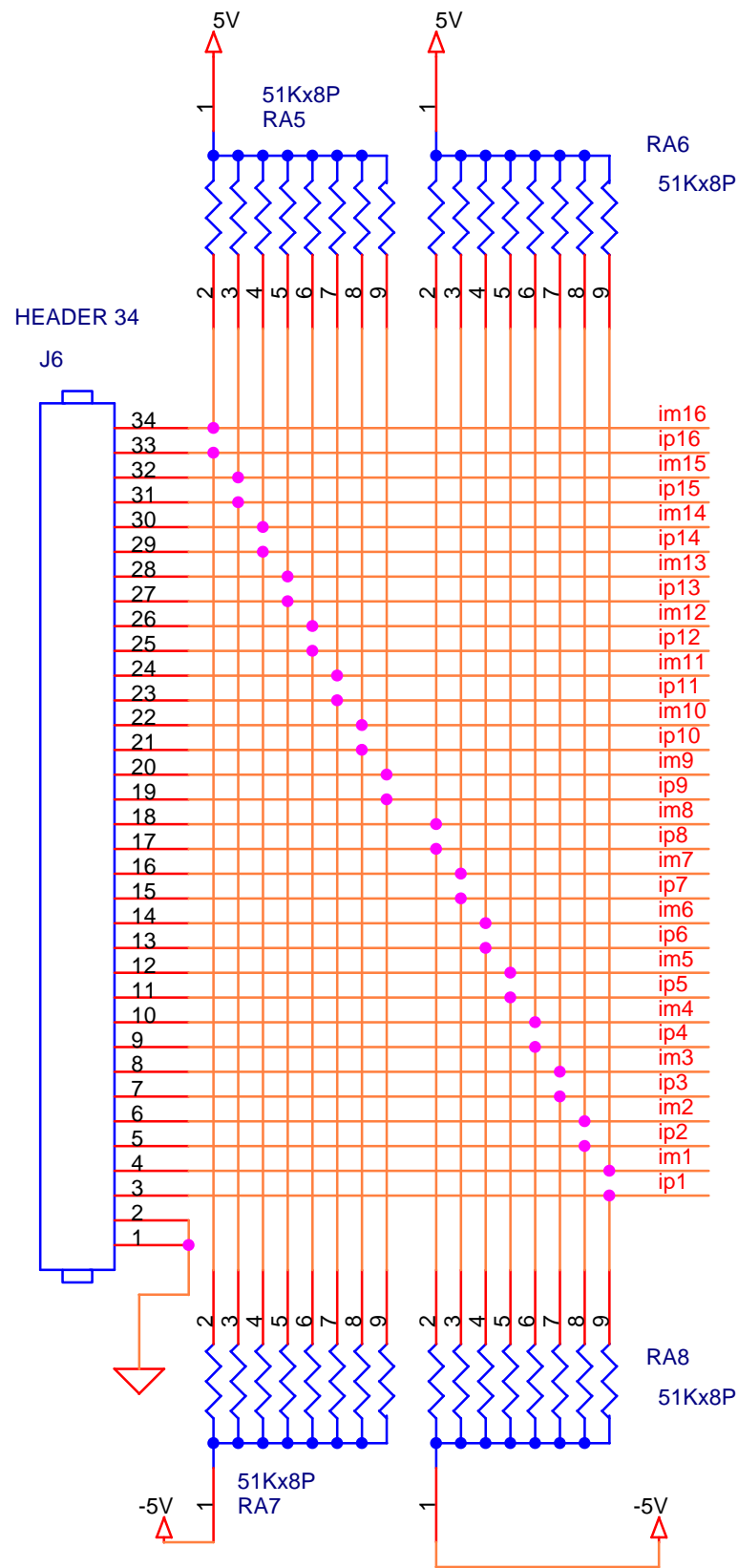
8出力を16出力へ変更し1番ピンの出力にNIM出力  
を付け加えた (4月 26日 2002年)



SN75110AN追加  
LEMOコネクタ追加  
(4月 26日 2002年)

Title		
OBUF : LVDS output driver block		
Size	Document Number	Rev
A4	MT-02222001-GTRGB	0
Date:	Wednesday, May 01, 2002	Sheet 3 of 12

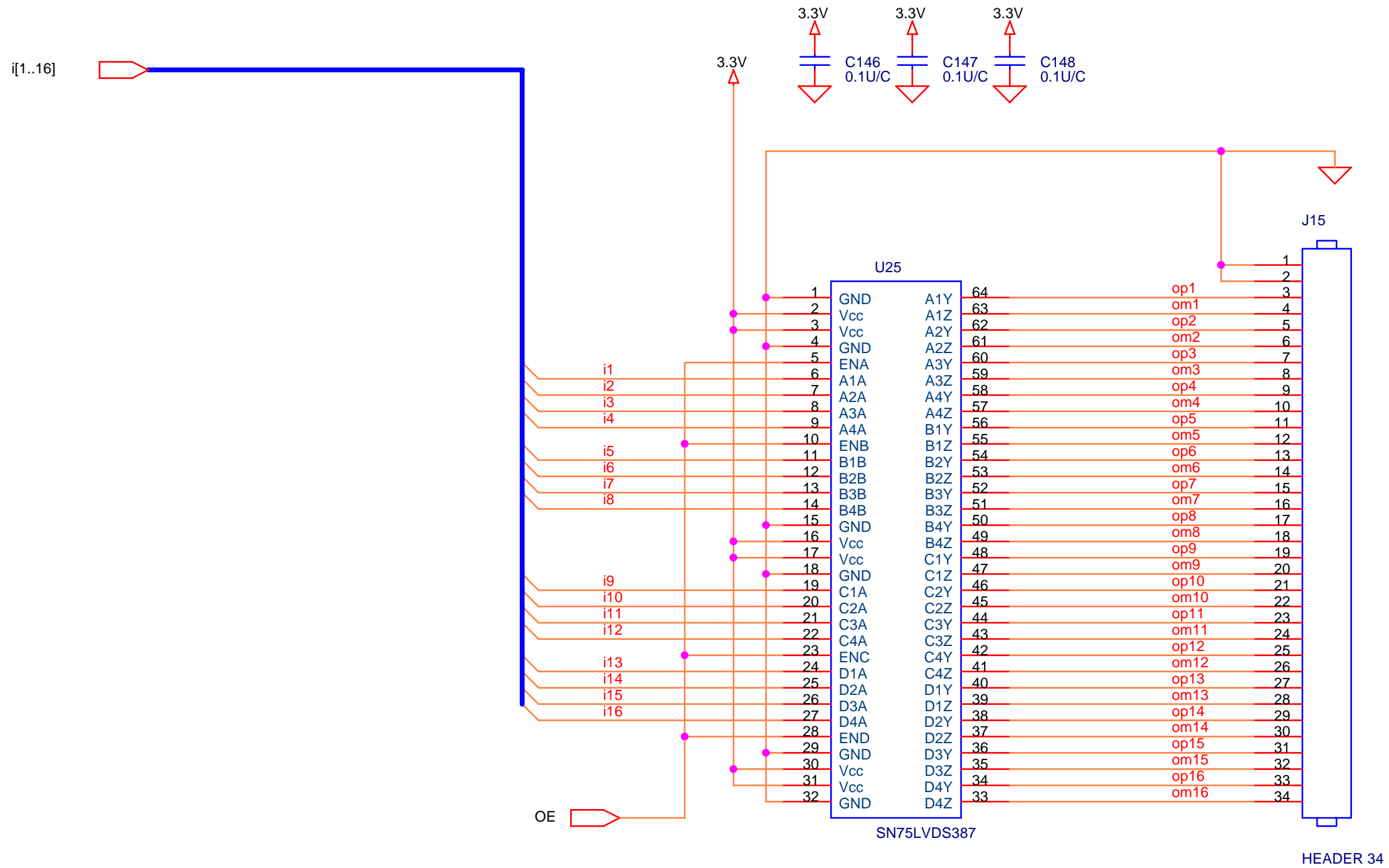
# ターミネータはなるべくICの近くへ実装



Check: connector pin assignement, receiver pin assignment  
5/29/2001

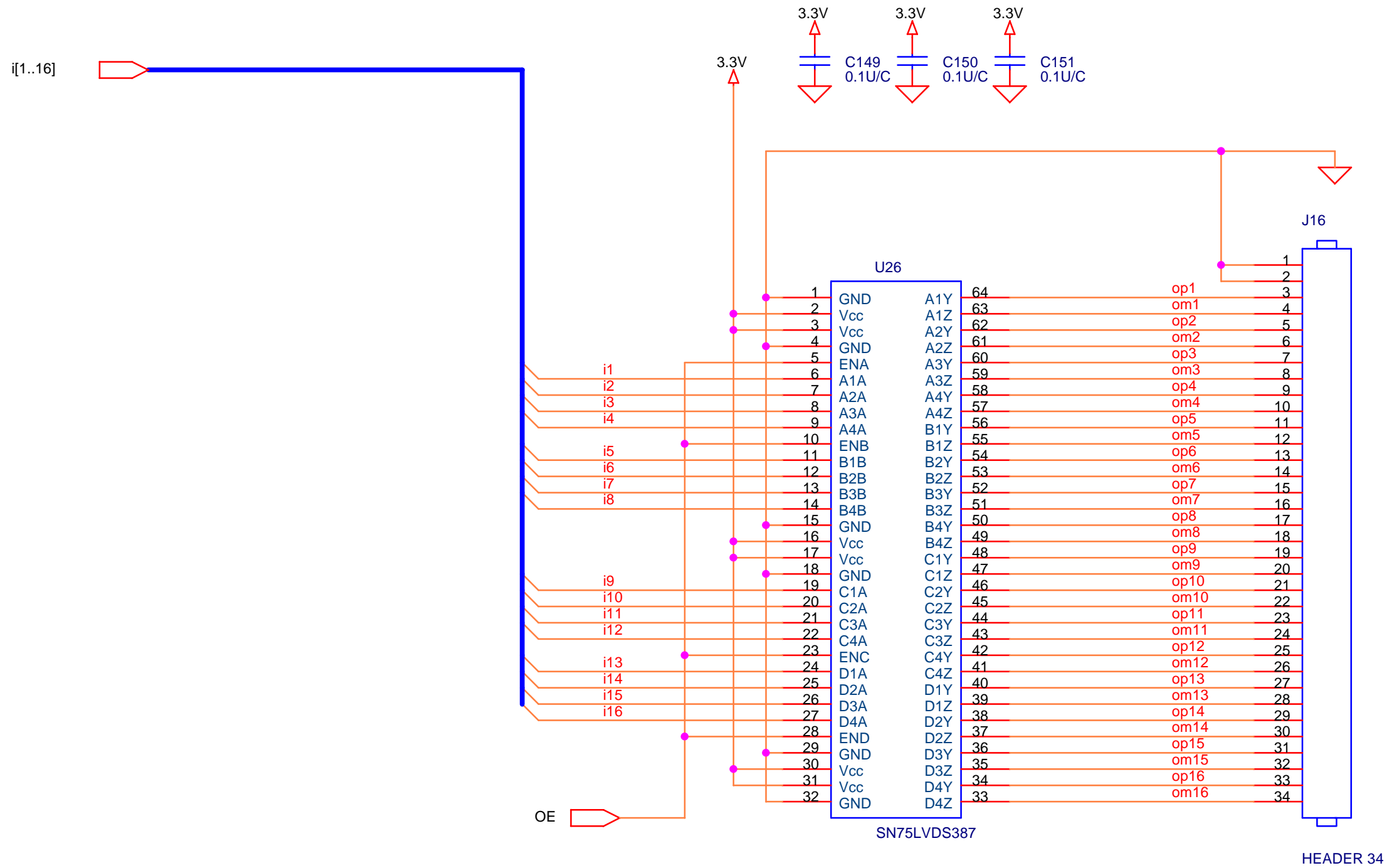
Title		
RECV16:receiver block		
Size	Document Number	Rev
A4	MT-02222001-GTRG	0
Date:	Sunday, January 27, 2002	Sheet 4 of 12

# 出力ブロックに変更 (Jun. 15. 2002)



Title			<b>LVDS driver</b>		
Size	Document Number	Rev			
A4	MT-06152001-GTRG2	<RevCode>			
Date:	Monday, June 17, 2002	Sheet	5	of	12

# 出力ブロックに変更 (Jun. 15. 2002)



Title		
<b>LVDS driver</b>		
Size	Document Number	Rev
A4	MT-06152001-GTRG2	<RevCode>
Date:	Monday, June 17, 2002	Sheet 6 of 12

D

D

C

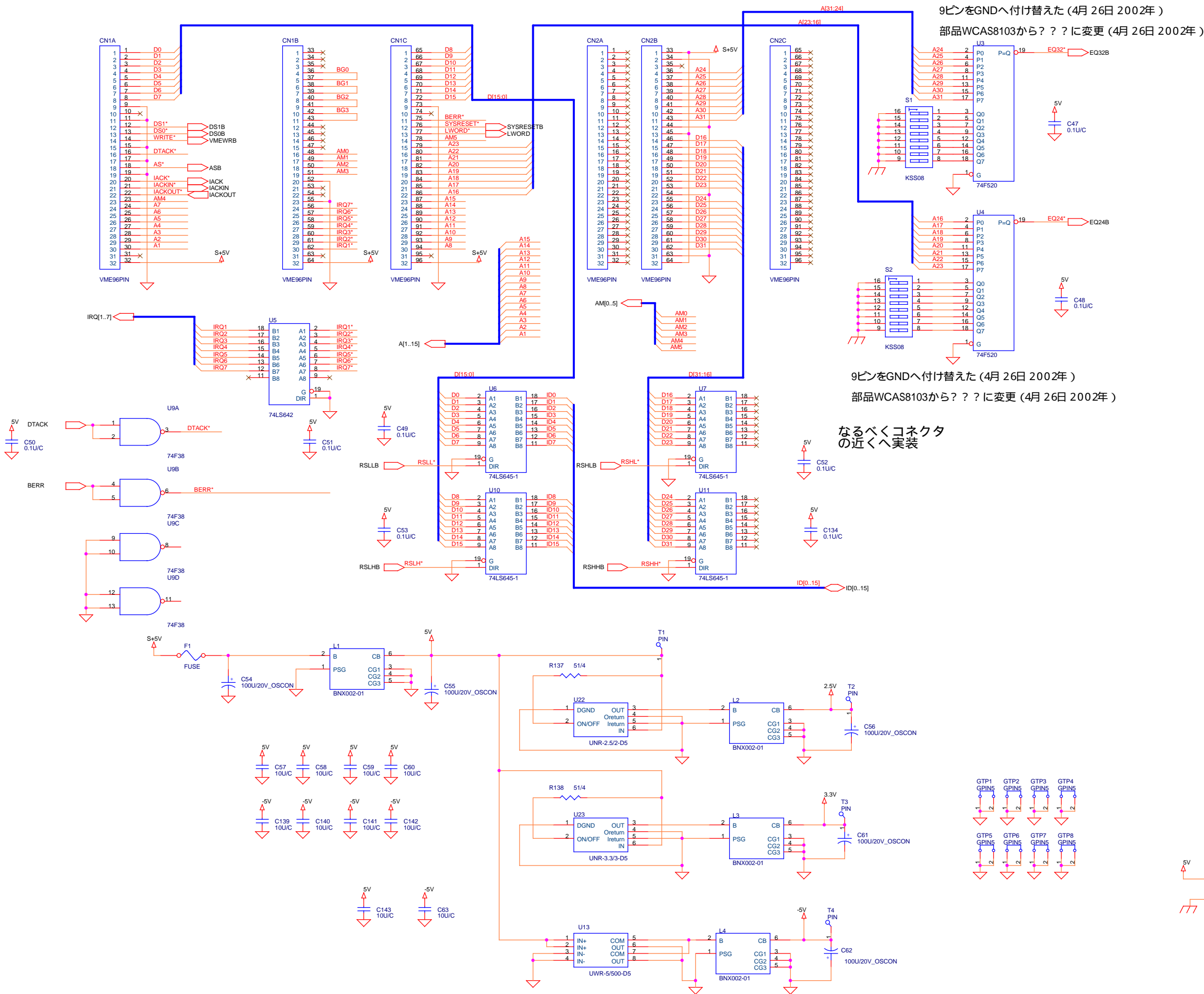
C

B

B

A

A



9ピンをGNDへ付け替えた(4月26日2002年)  
 部品WCAS8103から???に変更(4月26日2002年)

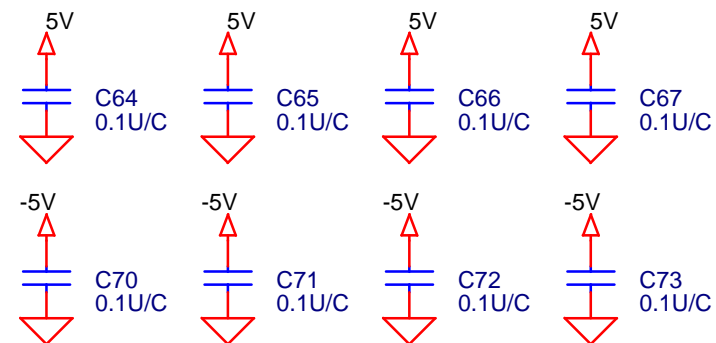
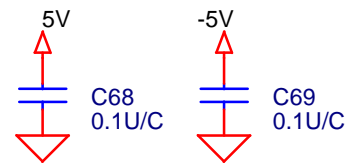
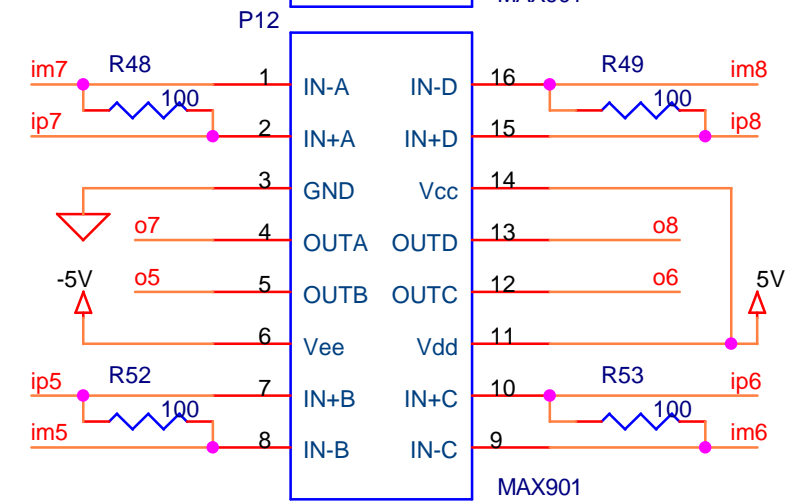
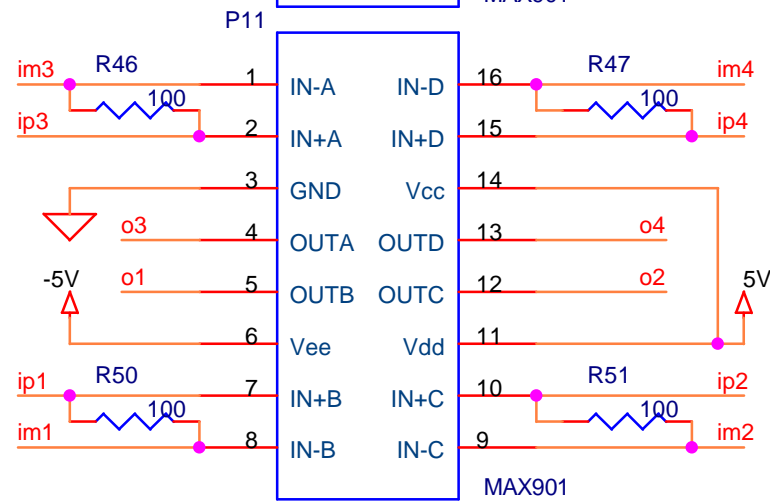
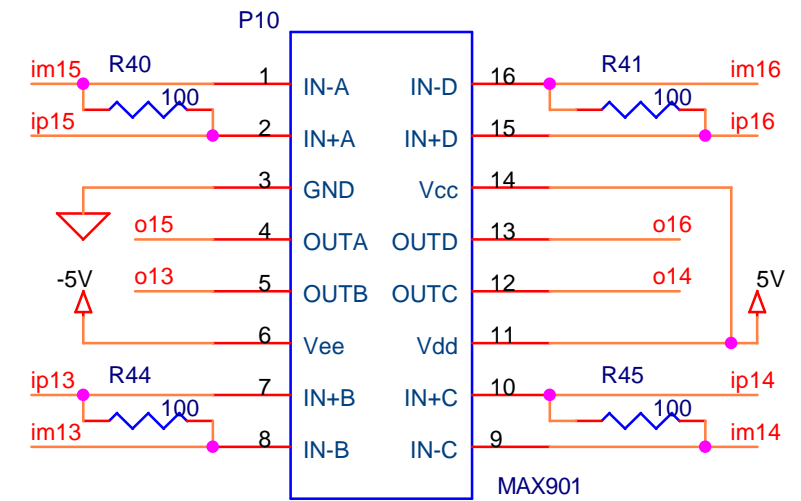
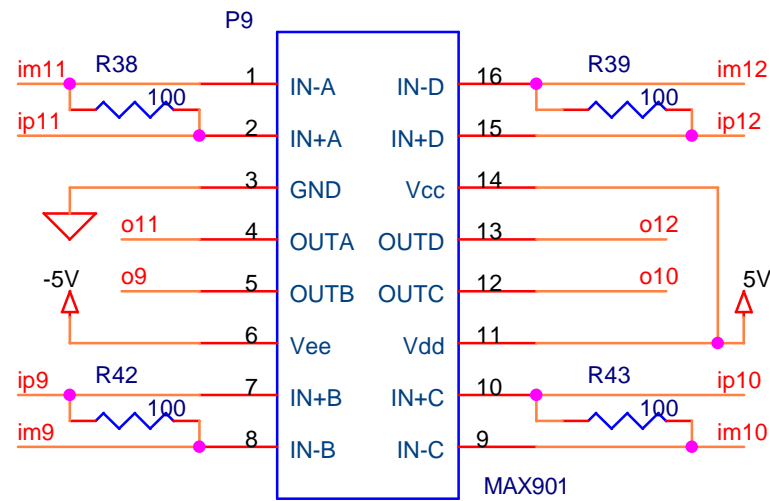
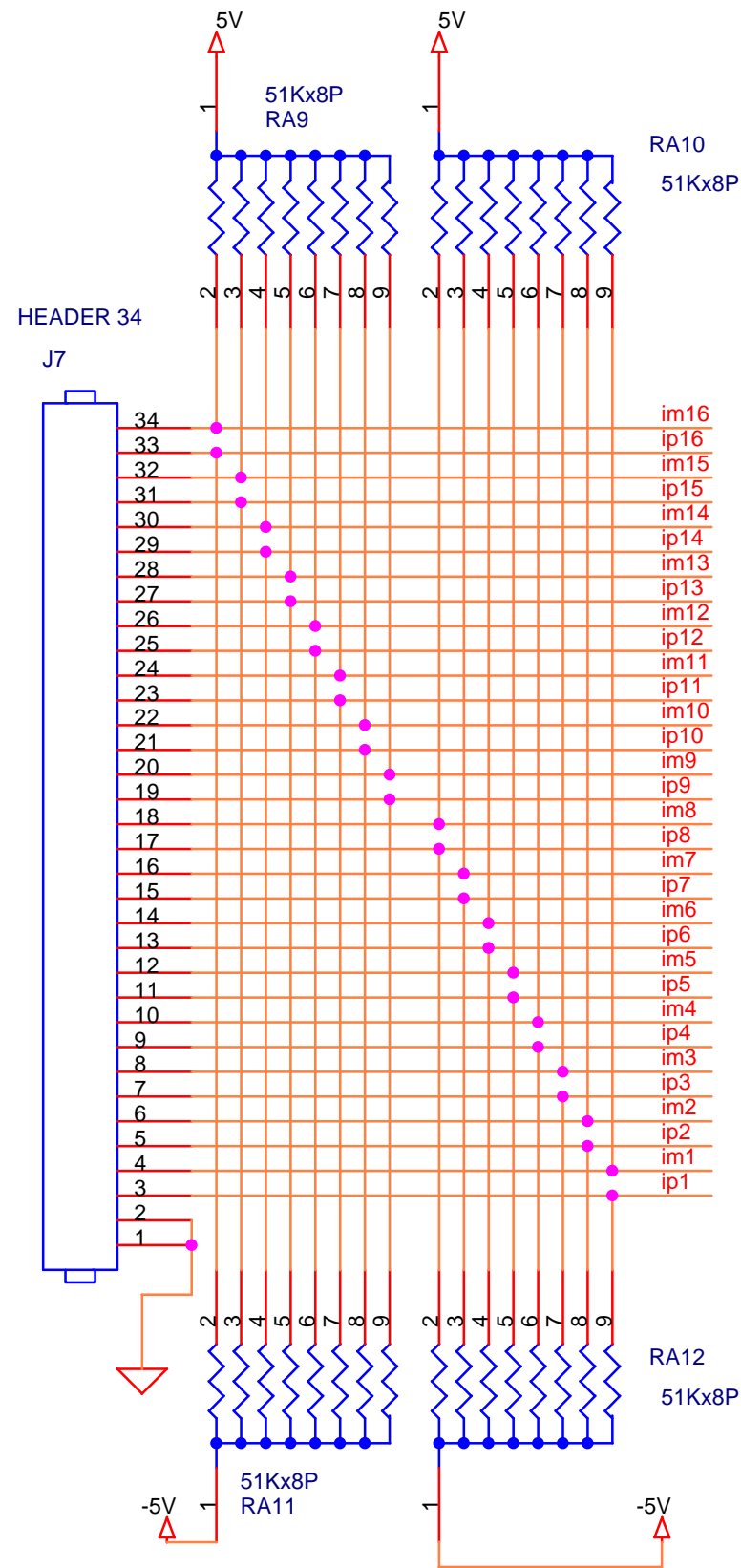
9ピンをGNDへ付け替えた(4月26日2002年)  
 部品WCAS8103から???に変更(4月26日2002年)

なるべくコネクタ  
 の近くへ実装

Title		VMEIF : VME interface block
Size	Document Number	MT-02222001-GTRG
Date	Wednesday, May 01, 2002	Sheet 7 of 12
Rev		-1



# ターミネータはなるべくICの近くへ実装

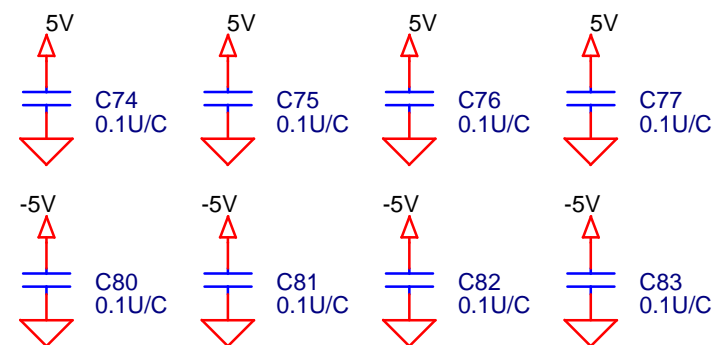
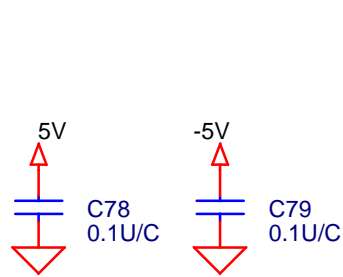
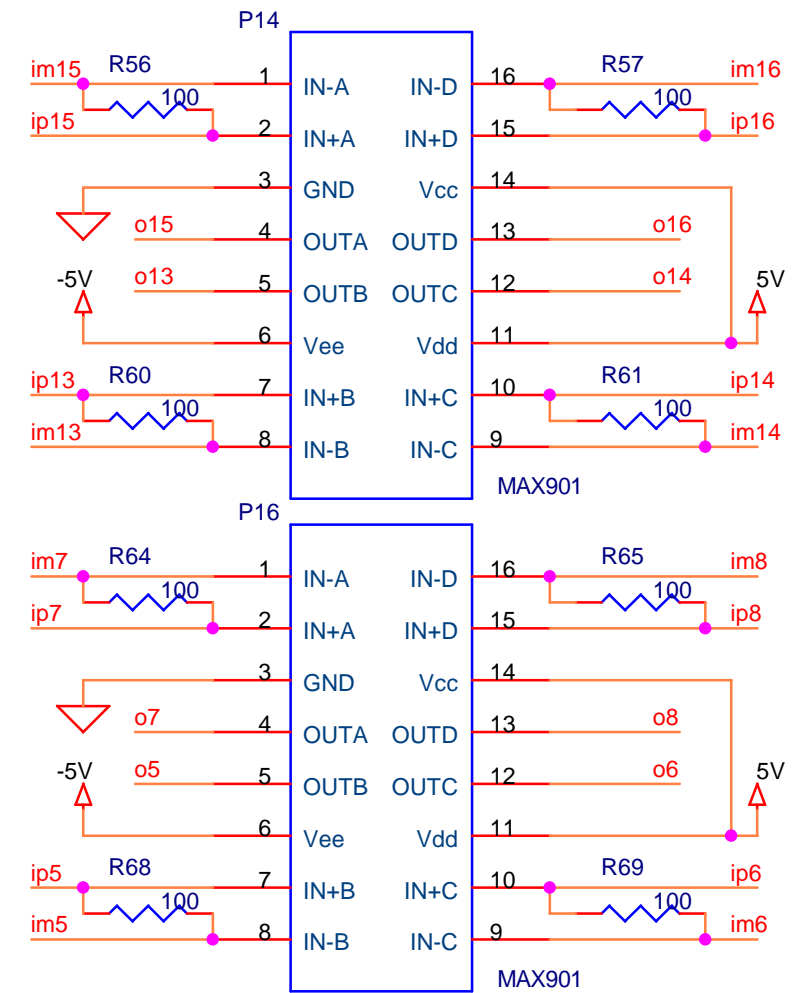
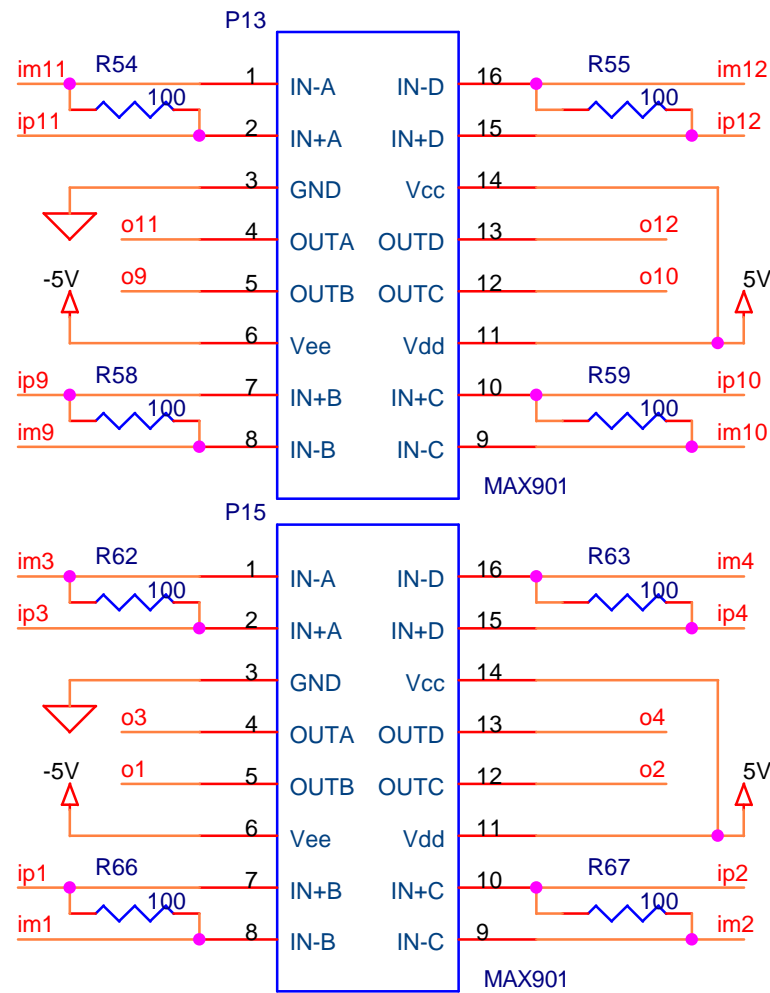
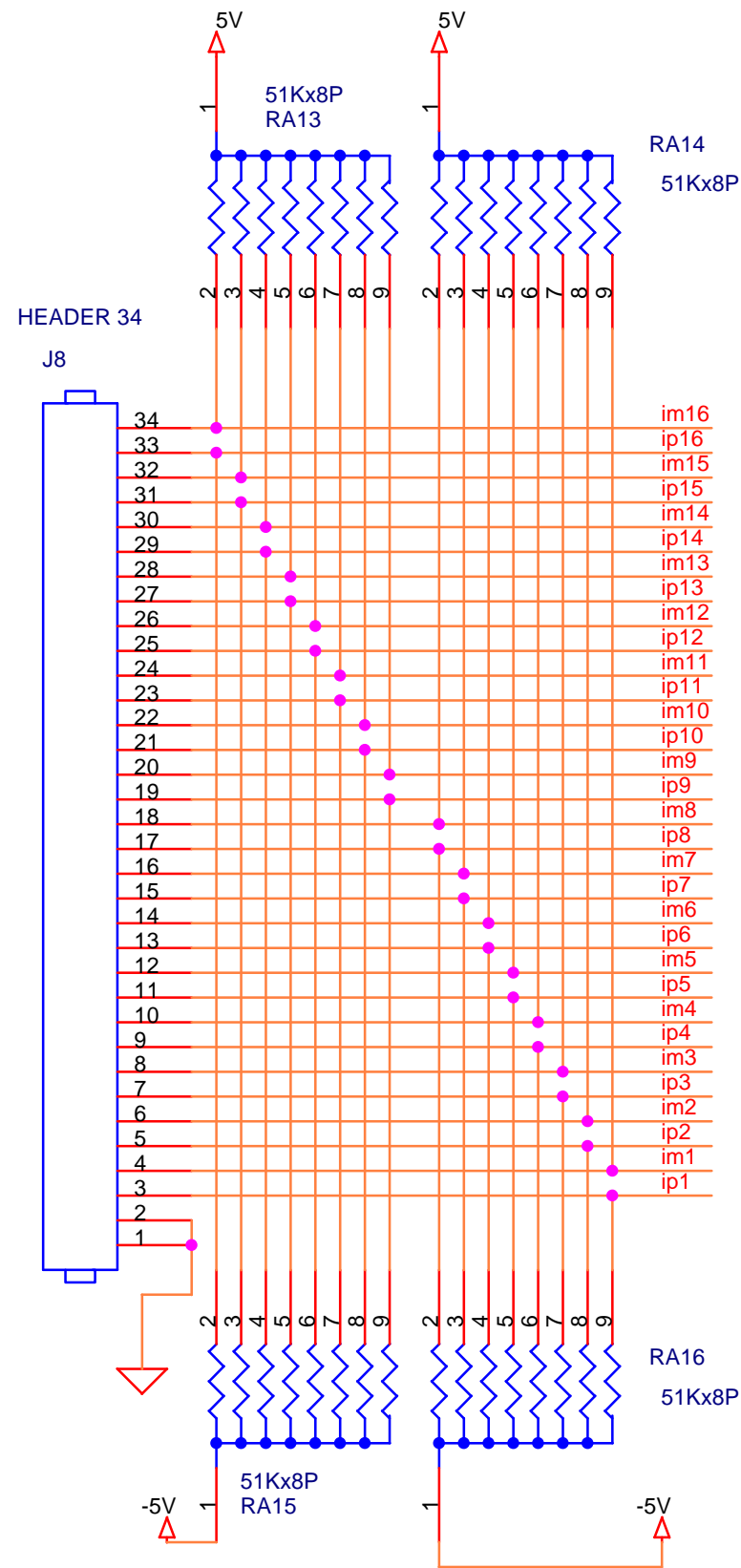


Check: connector pin assignement, receiver pin assignment  
5/29/2001

Title		
RECV16:receiver block		
Size	Document Number	Rev
A4	MT-02222001-GTRG	0
Date:	Sunday, January 27, 2002	Sheet 8 of 12



# ターミネータはなるべくICの近くへ実装

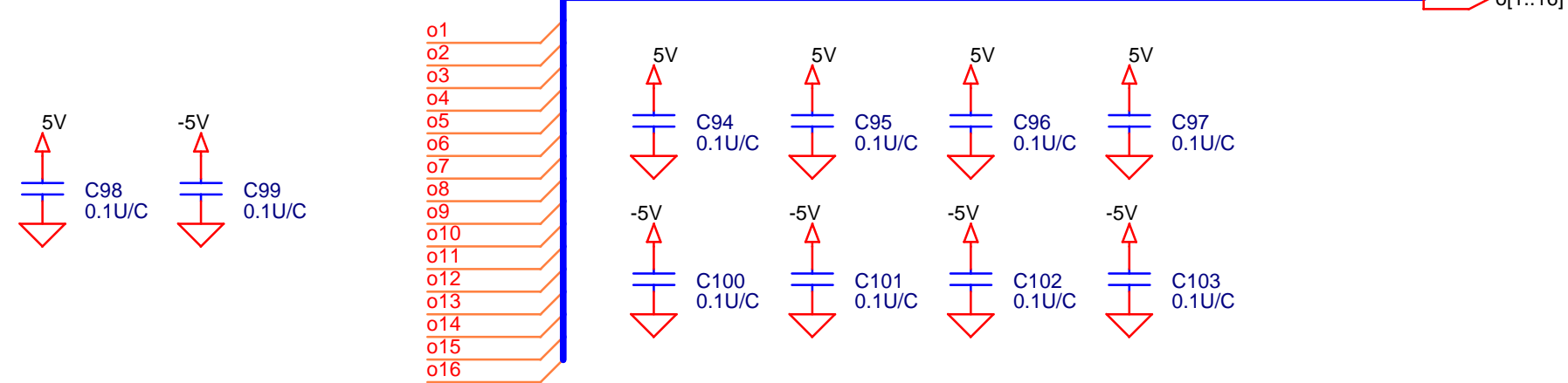
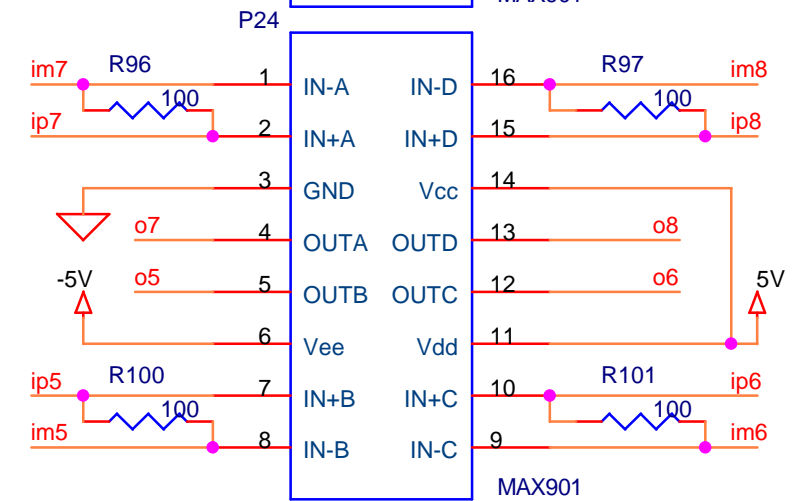
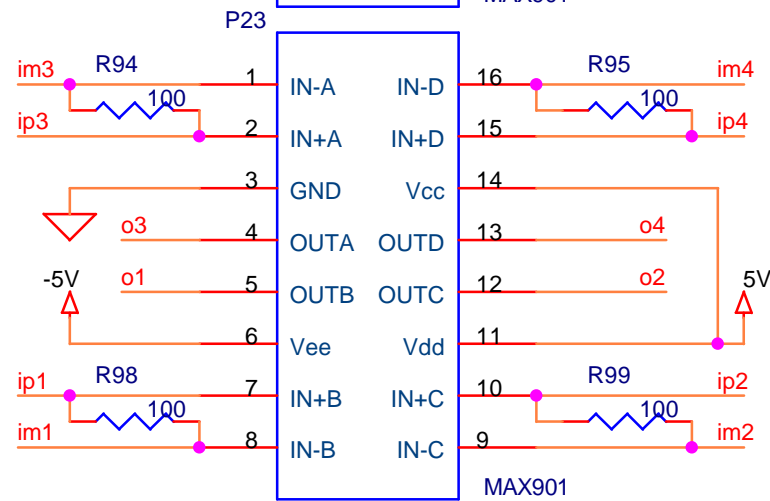
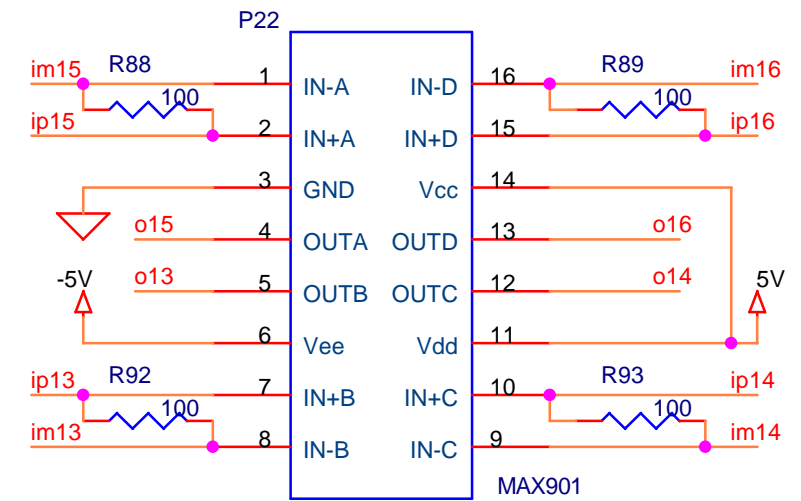
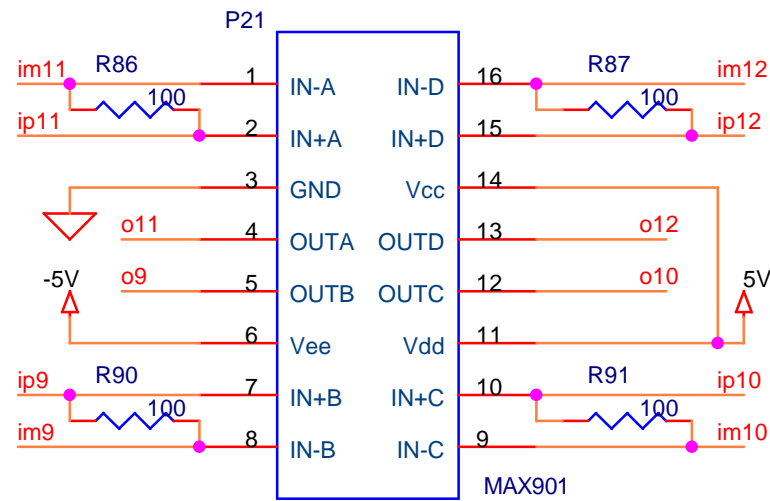
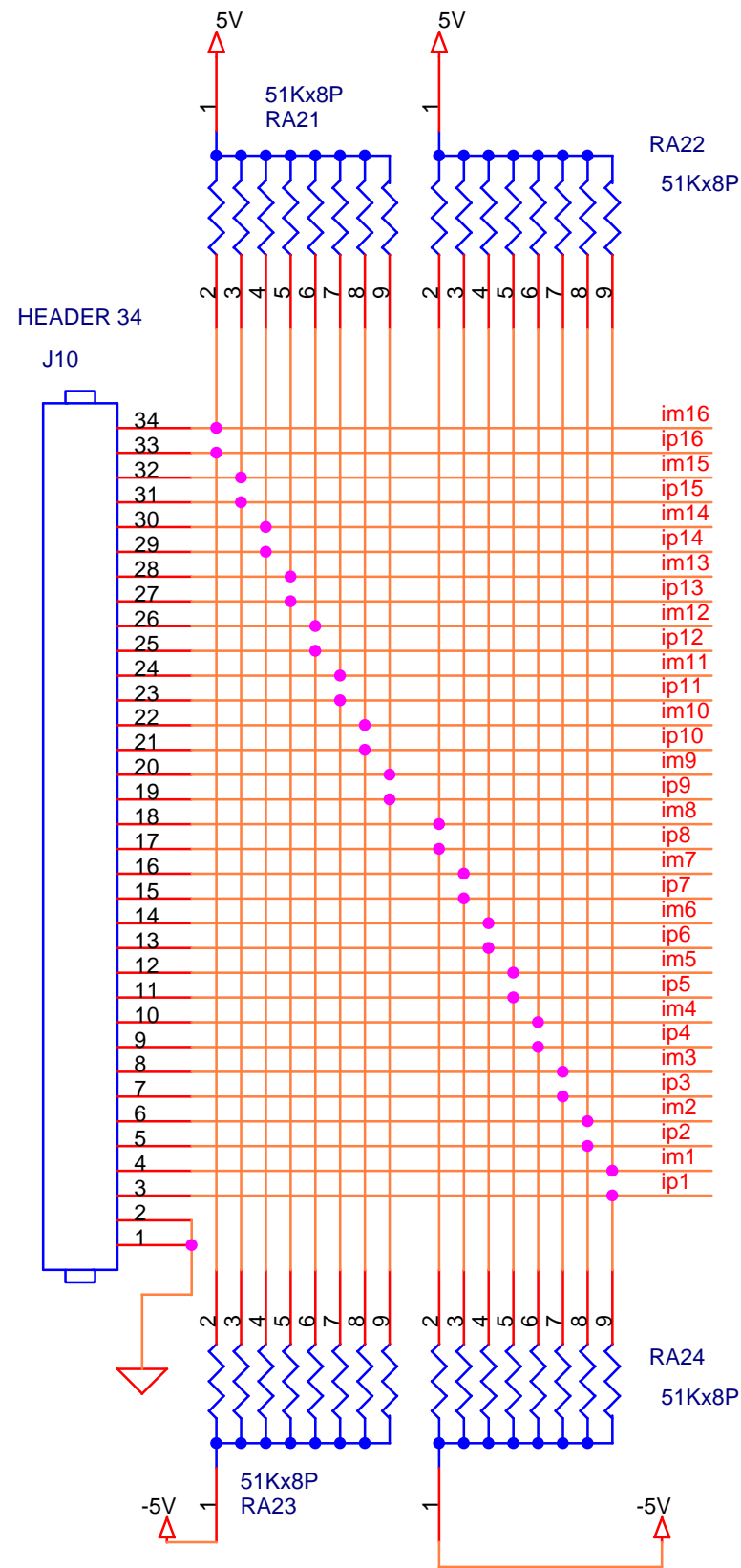


Check: connector pin assignement, receiver pin assignment  
5/29/2001

Title		
RECV16:receiver block		
Size	Document Number	Rev
A4	MT-02222001-GTRG	0
Date:	Sunday, January 27, 2002	Sheet 9 of 12

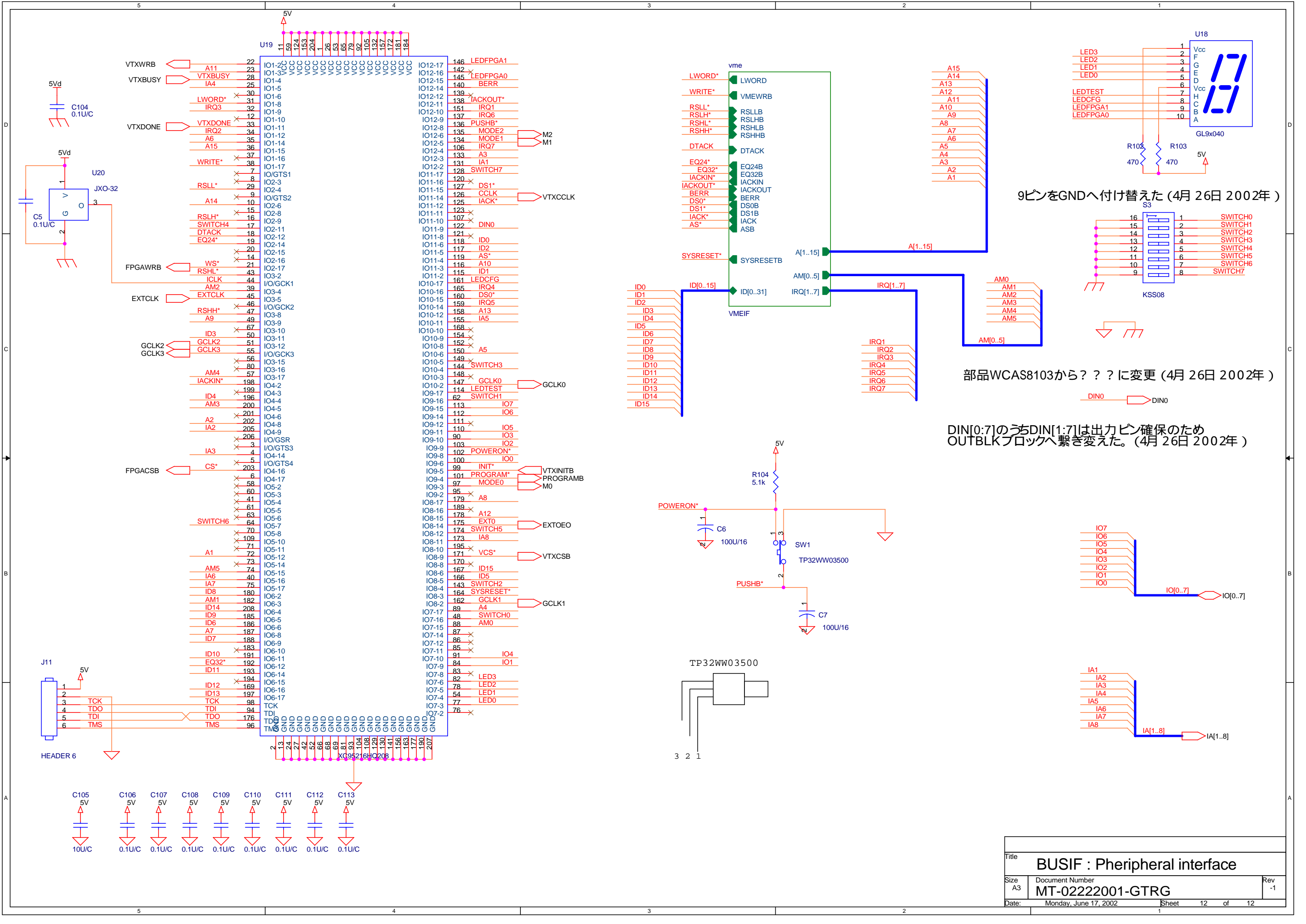


# ターミネータはなるべくICの近くへ実装



Check: connector pin assignement, receiver pin assignment  
5/29/2001

Title		
RECV16:receiver block		
Size	Document Number	Rev
A4	MT-02222001-GTRG	0
Date:	Sunday, January 27, 2002	Sheet 11 of 12



9ピンをGNDへ付け替えた(4月26日2002年)

部品WCAS8103から?? ?に変更(4月26日2002年)

DIN[0:7]のうちDIN[1:7]は出力ピン確保のため  
OUTBLKブロックへ繋ぎ変えた。(4月26日2002年)

Title			BUSIF : Peripheral interface		
Size	Document Number		Rev		
A3	MT-02222001-GTRG		-1		
Date:	Monday, June 17, 2002	Sheet	12	of	12